

# TRANSMITTAL OF FORMAL DRAWINGS

Docket No.  
907A.0145.U1(US)

In Re Application Of: **Ertel et al.**

JUN 14 2004  
U.S. PATENT & TRADEMARK OFFICE

Serial No.	Filing Date	Confirmation No.	Examiner	Art Unit
10/783,048	2/19/2004	6798		2133

Invention: **Phase Error Detector Using I/Q Interference Cancellation**

Address to:  
**Commissioner for Patents**  
P.O. Box 1450  
Alexandria, VA 22313-1450

Transmitted herewith are:

5 sheets of formal drawing(s) for this application.

Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).



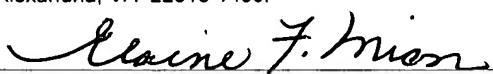
Signature

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Dated: June 8, 2004

I certify that this document and attached formal drawings are being deposited on 6/8/04 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



Signature of Person Mailing Correspondence

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